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APPLICATION N	IO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/211,677		12/14/1998	HYUN CHANG LEE	8733D-7153	9588
30827	7590	05/02/2003			
		G & ALDRIDGE LI	EXAMINER		
1900 K STREET, NW WASHINGTON, DC 20006				NGUYEN, KEVIN M	
				ART UNIT	PAPER NUMBER
				2674	カーフ
•			DATE MAILED: 05/02/2003		

Please find below and/or attached an Office communication concerning this application or proceeding.

Sy

	Application No.	Applicant(s)					
Advisory Action	09/211,677	LEE, HYUN CHANG					
Advisory Action	Examiner	Art Unit					
	Kevin M. Nguyen	2674					
The MAILING DATE of this communication appe		correspondence address					
THE REPLY FILED 18 April 2003 FAILS TO PLACE THI Therefore, further action by the applicant is required to avinal rejection under 37 CFR 1.113 may only be either: (1) condition for allowance; (2) a timely filed Notice of Appeal Examination (RCE) in compliance with 37 CFR 1.114.	oid abandonment of this application a timely filed amendment which	ation. A proper reply to a h places the application in					
PERIOD FOR RE	PLY [check either a) or b)]						
 a)	Advisory Action, or (2) the date set forth ater than SIX MONTHS from the mailin FILED WITHIN TWO MONTHS OF The date on which the petition under 37 CF	g date of the final rejection. HE FINAL REJECTION. See MPEP R 1.136(a) and the appropriate extension					
fee have been filed is the date for purposes of determining the period of fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of (2) as set forth in (b) above, if checked. Any reply received by the Office timely filed, may reduce any earned patent term adjustment. See 37 C	the shortened statutory period for reply ce later than three months after the mai CFR 1.704(b).	originally set in the final Office action; or ling date of the final rejection, even if					
1. A Notice of Appeal was filed on Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37 CFR 1.191(d)), to avoid dismissal of the appeal.							
2. The proposed amendment(s) will not be entered because:							
(a) ☑ they raise new issues that would require further consideration and/or search (see NOTE below);							
(b) they raise the issue of new matter (see Note below);							
(c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or							
(d) $oxed{oxed}$ they present additional claims without canceling a corresponding number of finally rejected claims.							
NOTE: <u>See Continuation Sheet</u> .							
3. Applicant's reply has overcome the following rejection(s):							
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a se	eparate, timely filed amendment					
5. The a) affidavit, b) exhibit, or c) request for reconsideration has been considered but does NOT place the application in condition for allowance because:							
6. The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.							
7.⊠ For purposes of Appeal, the proposed amendment(s) a)⊠ will not be entered or b)☐ will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.							
The status of the claim(s) is (or will be) as follows:							
Claim(s) allowed: <u>27-55</u> .							
Claim(s) objected to:							
Claim(s) rejected:							
Claim(s) withdrawn from consideration:	Λ						
8. The proposed drawing correction filed on is a) approved or /) disapproved by the Examiner.							
9. Note the attached Information Disclosure Statemer	nt(s)(P f p- 4 /9) H aper No(s)	.					
10. Other:							
SUPE	RICHARD HJERPE RVISORY PATENT EXAMINER	Kevin M. Nguyen Examiner Art Unit: 2674					

U.S. Patent and Trademark Office PTO-303 (Rev. 04-01)

TECHNOLOGY CLATER 2600 Advisory Action





Continuation of 2. NOTE: Claims 56-81 raise new issue "a first gate voltage reducing a voltage level substantially to a threshold voltage level".